

Rationality Validation of Business Process Model by Simulation Method

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ABSTRACT

Business process modeling has been adopted widely. Due to the complexity, it is very hard to validate the rationality of process models. The existing validation methods can only detect structural conflicts in process models. It is also very important to validate those objects related to the processes. This paper presents a rationality validation method which is based on discrete event simulation technology. It can detect three logic mistakes from business process models: structural deadlock, lack of synchronization and objects not matched each other. This method has extended the scope of rationality validation, and also enriches the contents that can be validated.

Keywords: Business Process Models, Rationality Validation, Simulation

1. INTRODUCTION

Business process model is kind of description of business process. By using model, we are able to analyze the performance of process, to guide the implementation of process, to monitor the execution of process, to enable the management of process. Due to the complexity of business process, whether describing AS-IS models or designing TO-BE models, it is possible to make kinds of mistakes about rationality. It is nonsense to analyze the performance of a process based on the wrong models, and implementing a wrong model may bring huge losing to an enterprise. Therefore, more and more attention has been pay to the validation of business process model.

Validation of a business process model is not so easy. If the structure and size are not limited, it will be an NP hard problem to validating a process model^[1]. Thus many efforts have been pay to how to improve the efficiency of validation algorithm, hoping to get high efficiency through simplifying model or adding restrict conditions. H. Lin^[2] gave a graphic reduction method to validate the mistakes of structural deadlock and lack of synchronization of a process based on his process modeling language. W.M.P.^[3] defined a WF-NET based on Petri-Net, then used Petri-Net theories to validate the rationality of the structure of a process. These methods have gain efficiency of multinomial time complexity. But problems raised here: Firstly, there are rigorous conditions for the algorithms, the method of H. Lin can validate only some specified simple structure of process, method based on Petri-Net required that the process model must be a free-choice net. Secondly, these methods can only find some logic problem of process structure, which is only a little scope of process rationality validation. These methods may have theoretical values, but they are not the practical ones for the real business process.

To support validation of real business process, we have gone on a different way. We haven't cared too much about efficiency of algorithm, but have paid more attention to enlarge the scope of contents of validation, and to improve the practicability. We have developed a validation method based on simulation technology in our eIDEF3 business process model. This method can detect three logic mistakes in business process: structural deadlock, lack of synchronization and objects not matched each other. Compared to the existing methods, it is not so effective, but enough for most business process. And most important, it has extended the scope of rationality validation, and also enriches the contents that can be validated.

2. EIDEF3 PROCESS MODELING METHOD

IDEF3 is one of the IDEF series methods. It is used to capture and describe business process. It can support communication and comprehension between domain experts and modelers, and has been used widely in industry. To increase its description ability, support simulation analysis, enlarge model usability, we have extended IDEF3 to eIDEF3 (extended IDEF3), which can describe objects involved in process in a formal way. We have developed a software tool GEM-EASY IDEF3[®] based on this method. Figure 1 is the basic syntactical elements of eIDEF3.

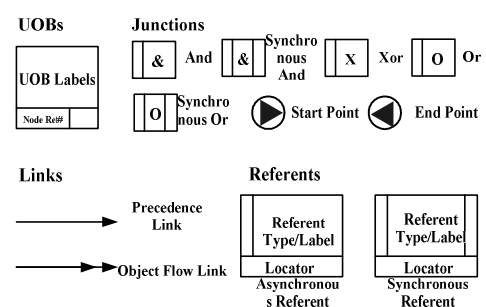
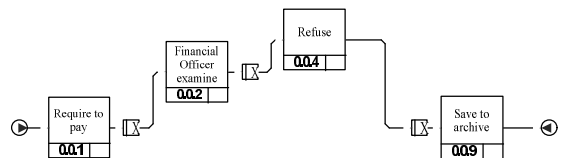
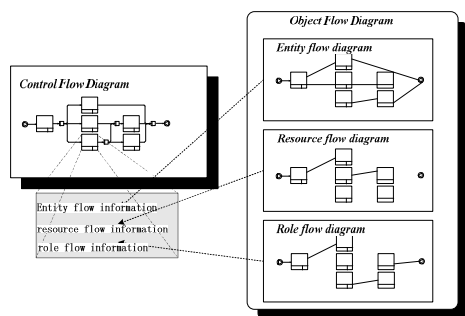


Figure 1 basic syntactical elements of eIDEF3

eIDEF3 provides two type basic flow diagram to describe a process scenario (As shown in Figure 2): One is Control Flow diagram, the other is Object Flow Diagram.



$$\begin{aligned}
&\forall n \in N, \\
&\text{if } (n \neq \text{BeginOf}(pis) \wedge n \neq \text{EndOf}(pis)) \\
&\text{then } (n \notin PB \wedge n \notin PE)
\end{aligned} \quad (5)$$

It is obviously that this definition of process nationality emphasizes the normally beginning and normally ending of a process. This is consistent with people's intuitive comprehension. More important, this definition is given from process itself, and is not bounded with specified modeling methods. So it is more generic. Moreover, defining process rationality based on PIS has built the foundation to validate it by using simulation method.

4. PROCESS RATIONALITY CRITERION IN EIDEF3 MODEL

Before give the concrete algorithm, we will give the criterion based on some precondition and assumption.

4.1 Basic precondition and assumption

Based on the requirement of eIDEF3 syntactical rules, the following basic precondition must be satisfied.

1) An eIDEF3 model P is an all connected diagram from Start Point to End Point. No isolated nodes exist. That is:

$$\begin{aligned}
&\forall n \in P, \\
&\text{InOf}(n) \geq 0 \vee \text{OutOf}(n) \geq 0
\end{aligned} \quad (6)$$

2) Connection rules: The Precedence Links of UOB are single in and single out. The Precedence Links of FanOut Junction are single in and multi out. The Precedence Links of FanIn Junction are multi in and single out. The Precedence Links of Start Point are none in and multi out. The Precedence Links of End Point are multi in and none out. That is:

$$\begin{aligned}
&\forall n \in P, \\
&\text{if } (n \in \text{UOB}) \text{ then } (\text{InOf}(n) \leq 1 \wedge \text{OutOf}(n) \leq 1) \\
&\text{if } (n \in \text{FanOut_Junction}) \text{ then } (\text{InOf}(n) \leq 1) \\
&\text{if } (n \in \text{FanIn_Junction}) \text{ then } (\text{OutOf}(n) \leq 1) \\
&\text{if } (n = \text{Start_Point}) \text{ then } (\text{InOf}(n) = 0) \\
&\text{if } (n = \text{End_Point}) \text{ then } (\text{OutOf}(n) = 0)
\end{aligned} \quad (7)$$

Moreover, we give some basic assumption here, which is only for facilitating description of validation problems, and will not influence the applicability of the algorithm:

1) Synchronous and Asynchronous Junctions are not differentiated. Alternate from synchronous to asynchronous, or from asynchronous to synchronous, will not change the Process Rationality.

2) There is no "Or" junction in process models. Based on the semantic of the "Or" junction, we can use a combination of "And" and "Xor" junction to replace it.

3) There is no feedback in a process. We assume that a process is a DAG (Directed Acyclic Graph). If a feedback is needed, we can put the feedback parts into the decomposed scenario, so that we can avoid dealing with it when using the validation algorithm. Then the rationality of the feedback parts can be determined by other methods.

4.2 Criterion of Process Rationality

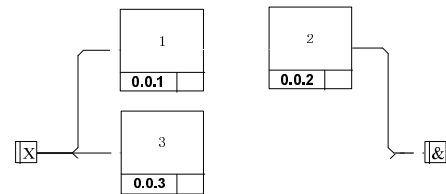
We have provided three criterions to validate process rationality, below are the details:

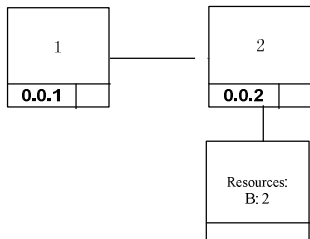
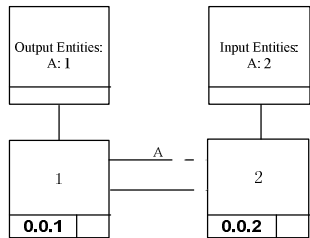
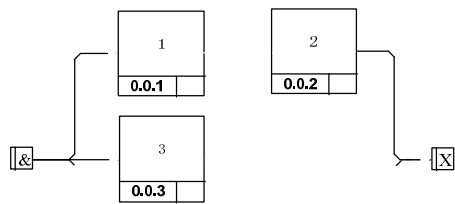
1) Structural Deadlock, SD

If two or more of the FanOut branches of "FanOut Xor" junctions joint in a "FanIn And" junction, SD will happen. That is:

$$\begin{aligned}
&\exists n \in \text{FanOut_Xor_Junction}, \\
&\text{OutOf}(n) \geq 2 \wedge \\
&\exists n' \in \text{FanIn_And_Junction}, \\
&n' \in \text{OutPath}(n)_i \wedge n' \in \text{OutPath}(n)_j, \text{ where } i \neq j \\
&\Leftrightarrow P \text{ is SD}
\end{aligned} \quad (8)$$

As shown in figure 4, in this situation, the "FanIn And" junction will not be able to be triggered because it can not finish all of its FanIn branch. Thus the process can not execute any more.





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ELSE
{
    Get a rational pis;
}

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4. After getting a rational pis, select randomly one of the rest FanOut branch from the tailender "FanOut Xor" junction, repeat step 3, create a new pis;

5. Repeat step 4, until all the "FanOut Xor" junction have been deal with. Then we get all the PISs of the process. Then we can conclude that this process is a rational one;

END

What have to point out is, this algorithm can find the mistake of rationality, and find the node where the mistake exists. But it can not correct the mistake automatically. You have to correct it manually, and execute the algorithm again, because new change may bring new mistakes in other place. Until no mistakes are found, you get a rational process finally.

5.2 Analysis of the algorithm

1) Completeness

From the step 4 and step 5, we can see that for a rational process, this algorithm can deal with all the fanout branch of the "FanOut Xor" junction. So we can say that this algorithm can explore all PISs of the process. Once there is mistake in the process, the algorithm will come to step 3. Here it can tell where the mistake is. Users can use this message to correct models, and run the algorithm again. Until all mistakes have been corrected, the process will become rational. According to the analysis of last paragraph, all the PISs of a rational process can be explored completely. Therefore, this algorithm is completed. It can explore all PISs of a process, and find all mistakes in it.

2) Complexity

It is easy to conclude that for a rational process, to explore all the PISs, the time consumed will increase exponentially along with the amount of "FanOut Xor" junction:

$$O = \bar{c}_{path}^{C_{FanOut_Xor_Junction}} \quad (14)$$

Here, \bar{c}_{path} is the average amount of fanout branch of the "FanOut Xor" junctions.

3) Usability

As we have mentioned at the beginning of this paper, most existing validation methods have many limitation in their usability. For example, the method of H. Lin can not validate complex models; the method of W.M.P requires the models must be free-choice. Our method has no rigorous limitation on process models, and the contents that can be validated are more abundant and complete.

Compared to those methods that are based on Petri-net, the complexity of our method is much better. For most

enterprises, a business process will not include too many "FanOut Xor" junctions (where decision making is needed). In the Chinese Aviation CIMS Project, we had build lots of business process models, among these models, the most complex model had no more the 10 "FanOut Xor" junctions. For today's computer, it is very easy to deal with such a complexity.

5. CONCLUSION

This paper presents a simulation-based business process rationality validation method. It has extended the scope of rationality validation, and also enriched the contents that can be validated. Although it is not the most efficient one, but for most business processes it is enough. So it is a validation method that is very suited for business process validation. Although this method is created based on eIDEF3, but the concepts about process rationality, process instance sub-graph, are independent of concrete modeling methods. So it is very easy to extend this method to other models, e.g. Petri-net models, workflow models, etc.

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